

**JX6-SB / JX6-SB-I**  
**Version Update**  
**from V. 2.14 to V. 2.15**



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# 1 Introduction

Version Updates - Survey			
Version	Function	upgraded	corrected
V 2.15	<p><i>JC -800</i></p> <p>Internal interface</p> <p><i>Register 3m02073</i></p> <p>Initialization value</p>		<p>✓</p> <p>✓</p>
V 2.14	<p><i>Expansion modules JX6-SB-I</i></p> <p>maxon EPOS 24/1</p> <p><i>Festo-CP-FB</i></p> <p>System bus configurations only with Festo-CP modules</p> <p><i>Intelligent JX2-Slave Modules</i></p> <p>Eight JX2 slave modules are connected to the system bus</p> <p><i>MC Applications</i></p> <p>Operating system update to intelligent JX2-slave modules</p>	<p>✓</p>	<p>✓</p> <p>✓</p> <p>✓</p>
V 2.13	<i>Motion Setup</i>		✓
V 2.12	<p><i>Fast inputs at JX2-ID8 / JX2-IO16</i></p> <p>The inputs of the expansion modules JX2-ID8 and JX2-IO16 can be configured as fast inputs</p>	✓	
	<p><i>Expansion modules JX6-SB(-I)</i></p> <p>- EX250-SCA1 by SMC</p>	✓	
	<p><i>Expansion modules JX6-SB-I</i></p> <p>- Vacon NX frequency converter</p>	✓	
	<p><i>Remanent values</i></p> <p>Baud rate, dummy module, etc., can also be stored on the JX6-SB(-I) module as remanent items. This is of special significance for JetControl 800.</p> <p><i>Error acknowledging and error recognition</i></p> <p>Error-LED reset and enquiry on the system bus statuses have been optimized.</p> <p><i>JX-SIO</i></p> <p>After starting the system busses, the values of the analog outputs will be read back.</p> <p>32-bit register overlay of the digital outputs</p>	<p>✓</p>	<p>✓</p> <p>✓</p>

V 2.11	<p><i>Expansion Modules</i></p> <p>The JX6-SB(-I) submodule now supports the following expansions in the master-slave mode JX6-SB ...</p> <ul style="list-style-type: none"> <li>- LJX7-CSL-108-ID16</li> <li>- LJX7-CSL-109-ID16-NPN</li> <li>- LJX7-CSL-107-OD8-2A</li> <li>- LJX7-CSL-113-ID8-OD8</li> <li>- LJX7-CSL-114-OD16</li> </ul> <p>- Milan Drives</p>	✓	
	<p><i>Monitoring JX2-I/O Modules</i></p> <p>The behaviour of the JX6-SB(-I) submodule can be configured</p>	✓	
	<p><i>JX2-Dummy-Slaves</i></p> <p>There is no timeout, if JX2-dummy-slaves are applied</p>	✓	
	<p><i>FESTO CP-FB Modules</i></p> <p>Parallel mode with JX-SIO is possible</p>	✓	
V 2.10	<p><i>JX6-SB Master-Slave Mode</i></p> <p>This has been newly added; it is a special feature of this mode that its IO and register numbers match those of NANO, respectively JC-24X.</p>	✓	
	<p><i>Expansion Modules</i></p> <p>The JX6-SB(-I) submodule now supports the following expansions in the master-slave mode JX6-SB ...</p> <ul style="list-style-type: none"> <li>- JX-SIO</li> <li>- Festo CPV-Direct</li> <li>- Festo CPX-Terminal Unit</li> <li>- SMC SI-Unit</li> <li>- Bürkert Valve Block</li> <li>- Lenze Frequency Converter</li> </ul>	✓	
	<p><i>Master-Master Mode</i></p> <p>A register for direct input of the latest register data number has been added</p> <p>now, 32-bit registers can be transferred</p>	✓	

**Important!**



While the operating system is being updated, the voltage supply of the controller must not be interrupted.

## Operating System Version 2.14



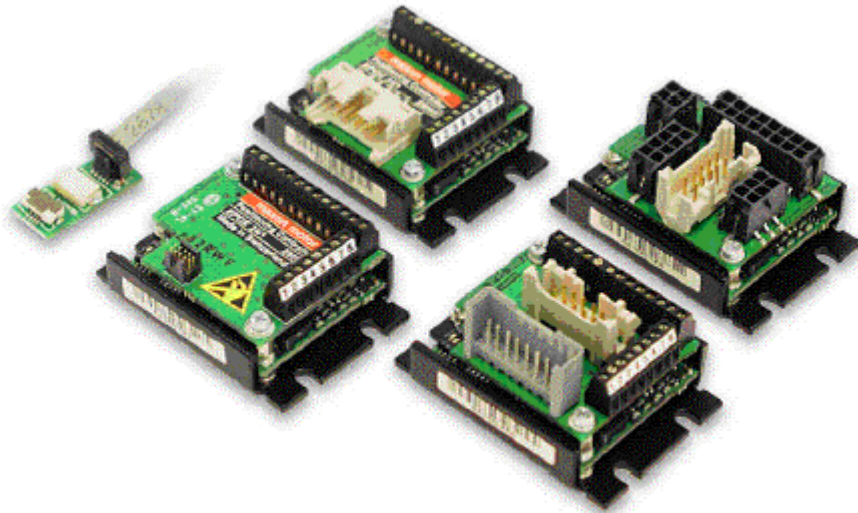
Any alterations made from operating system version 2.13 to 2.14 have also been listed in this documentation.

## 2 Expansions

### 2.1 EPOS 24/1- Positioning Control

EPOS 24/1 positioning controls of the maxon motor ag can be connected to the system bus directly. For information on how to connect the EPOS 24/1 positioning control to the system bus, please refer to the corresponding documentation.

#### EPOS 24/1- Positioning Control



Technical Data of Milan Drives	
Maximum amount of EPOS 24/1-positioning controls	10
Size of I/Os	16
Module code	75
Documentation	maxon_ba_100_betriebsanleitung

## **3 Eliminated Software Bugs**

### **3.1 FESTO CP-FB Modules**

If system bus configurations only consist of Festo CP-FB modules, input values of CP input modules can be read

### **3.2 Operating system update to intelligent JX2-slave modules**

In case of MC applications, an operating system update to intelligent JX-slave modules can be carried out.

### **3.3 Intelligent JX2-Slave Modules**

At the system bus of the JX6-SB-I, now up to eight intelligent JX2-slave modules can be recognized and operated again.

### **3.4 JX6-SB(-I) Submodule in Connection With JC-800**

Communication problems of the internal interface between the JC-800 and the JX6-SB(-I) submodule have been solved.

### **3.5 Register 3m02073 "JX-SIO Timeout Time"**

While the system bus is initialized, the JX6-SB(-I) submodule sets the JX-SIO timeout time written in register 3m02073 to a value of 50 ms. After initializing, the former value of register 3m02073 is restored.

As of V. 2.15, the value is also restored, if during initialization no JX-SIO modules, nor any modules supplied by other manufacturers, have been found.