**Quick Reference**

**Sumodule JX6-DA4**

### Registers

**JX6-DA4 is located in a D-CPU**

**Description of the register pattern:**

- **Submodule socket # 1:** \( y = 3 \)
- **Submodule socket # 2:** \( y = 4 \)
- **Submodule socket # 3:** \( y = 5 \)

- **61688** Setting submodule type of socket # 1
  - 4 DA4_MODULE_TYPE

- **61689** Setting submodule type of socket # 2
  - 5 DA4_MODULE_TYPE

- **61692** Setting submodule type of socket # 3
  - 4 DA4_MODULE_TYPE

- **63y01** Output value DAC channel # 1
- **63y02** Output value DAC channel # 2
- **63y03** Output value DAC channel # 3
- **63y04** Output value DAC channel # 4

Value range of DAC output: 
\(-32768 \ldots +32767\)

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**JX6-DA4 is located in a JC 647**

**Description of the register pattern:**

- **Submodule socket # 1:** \( y = 3 \)
- **Submodule socket # 2:** \( y = 4 \)
- **Submodule socket # 3:** \( y = 5 \)

- **61688** Setting submodule type of socket # 1
  - 4 DA4_MODULE_TYPE

- **61689** Setting submodule type of socket # 2
  - 5 DA4_MODULE_TYPE

- **61692** Setting submodule type of socket # 3
  - 4 DA4_MODULE_TYPE

- **63y01** Output value DAC channel # 1
- **63y02** Output value DAC channel # 2
- **63y03** Output value DAC channel # 3
- **63y04** Output value DAC channel # 4

Value range of DAC output: 
\(-32768 \ldots +32767\)

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**JX6-DA4 is located in JX6-CON1**

**Description of the register pattern:**

- **1xyzzz**
  - **x** specifies the slot where the JX6-CON1 basic module is located.
  - **y** specifies the socket on the JX6-CON1 module:
    - **zzz** specifies the register # itself
    - **0 .. 999**

- **1xy110** Output value DAC channel # 1
- **1xy111** Output value DAC channel # 2
- **1xy112** Output value DAC channel # 3
- **1xy113** Output value DAC channel # 4
- **1xy199** Detected submodule type
  - 5 DA4_MODULE_TYPE

Value range of DAC output:
\(-32768 \ldots +32767\)

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**Attention!**

A register description for a PID controller, as well as for a JX6-DA4 submodule located in socket # 2 combined with a JX6-AD8 submodule located in socket # 1 is given in the programming reference on the JetWeb PID controller module.
### Description of Connections

Analog outputs – 15-pin female SUB-D connector

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>GND</td>
<td>Reference potential</td>
</tr>
<tr>
<td>2</td>
<td>Not assigned</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>IOUT4</td>
<td>Current output - Channel # 4</td>
</tr>
<tr>
<td>4</td>
<td>IOUT3</td>
<td>Current output - Channel # 3</td>
</tr>
<tr>
<td>5</td>
<td>IOUT2</td>
<td>Current output - Channel # 2</td>
</tr>
<tr>
<td>6</td>
<td>IOUT1</td>
<td>Current output - Channel # 1</td>
</tr>
<tr>
<td>7</td>
<td>GND</td>
<td>Reference potential</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>Reference potential</td>
</tr>
<tr>
<td>9</td>
<td>GND</td>
<td>Reference potential</td>
</tr>
<tr>
<td>10</td>
<td>GND</td>
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</tr>
<tr>
<td>11</td>
<td>VOUT4</td>
<td>Voltage output Channel # 4</td>
</tr>
<tr>
<td>12</td>
<td>VOUT3</td>
<td>Voltage output Channel # 3</td>
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<tr>
<td>13</td>
<td>VOUT2</td>
<td>Voltage output Channel # 2</td>
</tr>
<tr>
<td>14</td>
<td>VOUT1</td>
<td>Voltage output Channel # 1</td>
</tr>
<tr>
<td>15</td>
<td>Not assigned</td>
<td></td>
</tr>
</tbody>
</table>

All voltage and current outputs are provided with GND reference!